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L1.clm.	3

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13 L1

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*DB=PGPB,USPT,USOC; PLUR=YES; OP=OR***Hit Count Set Name**

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L2 L1 same count\$370 L2L1 complement\$3 same data same bus same (threshold or limit)205 L1

END OF SEARCH HISTORY

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Search Results -

Terms	Documents
L1 same count\$3	0

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DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L5 L2 and L4

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L4 710/305,100,34,65;365/49,63,190,233.5,205;713/502;327/518;326/35,86;370/464,476;712/220.ccls. 13770 L4

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L3 L1 same count\$3

0 L3

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L2 L1 same count\$3

70 L2

L1 complement\$3 same data same bus same (threshold or limit)

205 L1

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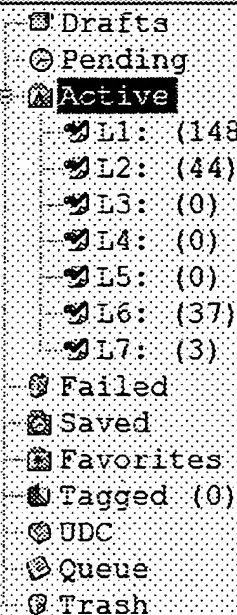
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 L1: (148) complement\$3
 L2: (44) 11 same count\$
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 L5: (0) 11 same (count\$
 L6: (37) 11 and (power
 L7: (3) 11 same (power
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11 same (power near3 consum\$5)

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2	<input type="checkbox"/>	<input type="checkbox"/>	US 5534800 A	19960709	21	Sense amplifier, SRAM, and microprocessor	327/57	327/51; 365/205
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5105104 A	19920414	5	Self-adjusting precharge level circuit	326/86	326/33; 326/98;



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2	BRS	L2	44	l1 same count\$3	USPA T	2006/03/0 3 12:40				
3	BRS	L3	0	l1 same (count\$3 near3 transition\$1)	USPA T	2006/03/0 3 12:41				
4	BRS	L4	0	l1 same (count\$3 near10 transition\$1)	USPA T	2006/03/0 3 12:42				
5	BRS	L5	0	l1 same (count\$3 near10 exceed\$3)	USPA T	2006/03/0 3 12:42				
6	BRS	L6	37	l1 and (power near3 consum\$5)	USPA T	2006/03/0 3 12:43				
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IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
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- ☐ 1. **IEEE standard for a versatile backplane bus: VMEbus**
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1987
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IEEE JNL IEE Journal or Magazine

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IEEE STD IEEE Standard

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